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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Sheet 1 of 2

Complete if Known

Application Number	10/878,845
Filing Date	10-01-2003
First Named Inventor	Gyung-Su CHO
Art Unit	2811
Examiner Name	Nadav, Ori
Attorney Docket Number	OPP030889US

U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number Number-Kind Code, ² if known	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
ON	CA	US- 6,710,460	03-23-2004	MOROZUMI	
	CB	US- 2003/0194877	10-16-2003	YAU et al.	
	CC	US- 8,620,721	09-16-2003	LEE	
	CD	US- 8,488,908	10-22-2002	CHAN et al.	
ON	CE	US- 5,571,752	11-05-1996	CHEN et al.	
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FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶

Examiner Signature	ON	Date Considered	6/10/06
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				Examiner Name	Nadav, Ori
Sheet	2	of	2	Attorney Docket Number	OPP030889US

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
ON	DA	Stanley WOLF, Ph.D. and Richard N. TAUBER, Ph.D.; Silicon Processing for the VLSI ERA; Volume 1: Process Technology; 2000; Pages 560-564; Lattice Press, Sunset Beach, CA, USA	

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